



09-30-04

IPW EV372461393

PTO/SB/21 (09-04)

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FORM**

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Total Number of Pages in This Submission

Application Number 10/766,376

Filing Date January 27, 2004

First Named Inventor Alan R. Reinberg

Art Unit 2824

Examiner Name Christian D. Wilson

Attorney Docket Number MI22-2491

ENCLOSURES (Check all that apply)

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EV372461393

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PRIORITY Application Serial No. 10/323,525
PRIORITY Filing Date December 18, 2002
Inventor Alan R. Reinberg
Assignee Micron Technology, Inc.
PRIORITY Group Art Unit 2824
PRIORITY Examiner Christian D. Wilson
Attorney's Docket No. MI22-2491
Title: Microelectronic Device Fabricating Methods, and Methods of Forming a Pair
of Field Effect Transistor Gate Lines of Different Base Widths From a Common
Deposited Conductive Layer

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4. Fee Transmittal (PTO/SB/17)
5. True copy of parent application comprising 31 total pages specification, claims and Abstract plus title page; drawings (Figs. 1-11 and Declaration.
6. 6 sheets of formal drawings, (Figs. 1 - 11).
7. Preliminary Amendment
8. Terminal Disclaimer
9. Information Disclosure Statement including PTO-1449

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MI22-2204 (MSM) 2491
Micron Technology, Inc.

Dated: January 27, 2004

The stamp of the U.S. Patent and Trademark Office acknowledges receipt of the application for letters patent of the following:

Applicant: Alan R. Reinberg
Titled: Microelectronic Device Fabrication Methods, and Methods of Forming a Pair of Field Effect Transistor Gate Lines of Different Base Widths From a Common Deposited Conductive Layer

1. Return Postcard Receipt.
2. A \$ 898.00 check
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